

FIG.1A

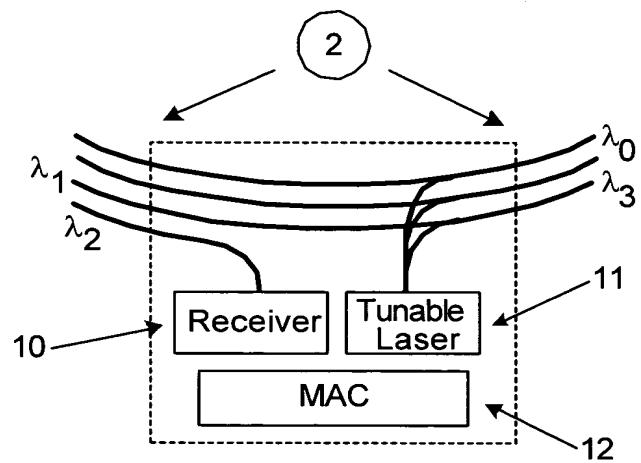


FIG.1B

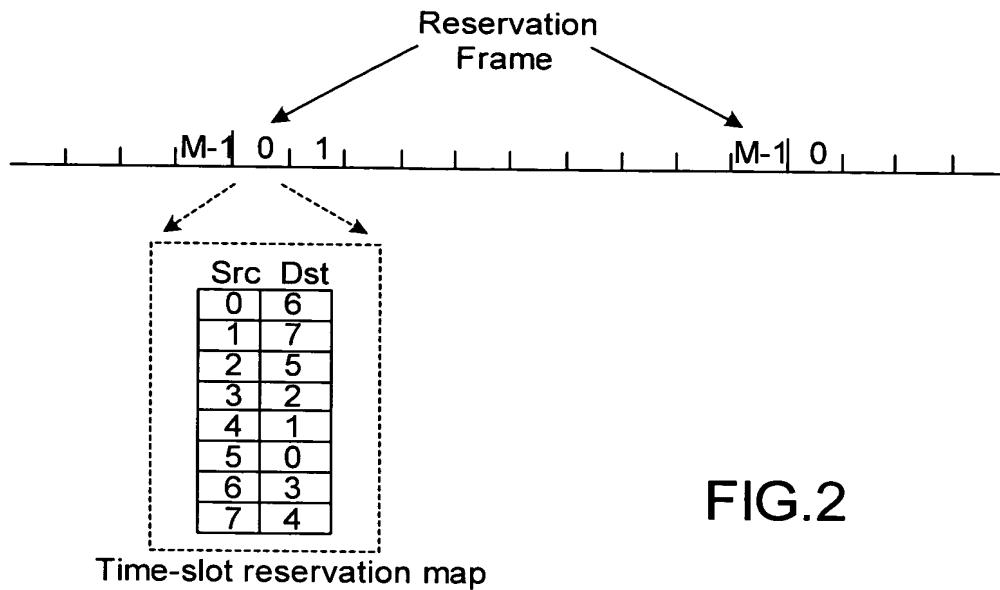
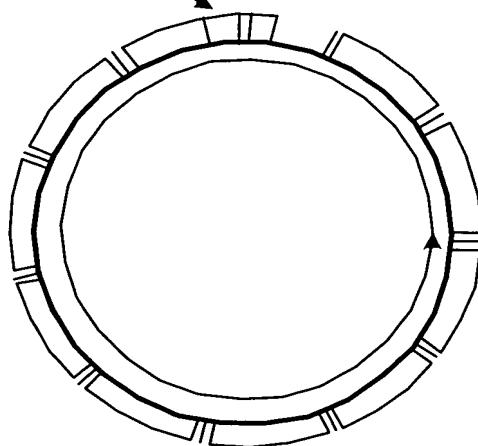


FIG.2

FIG.3

Time-slots overlap



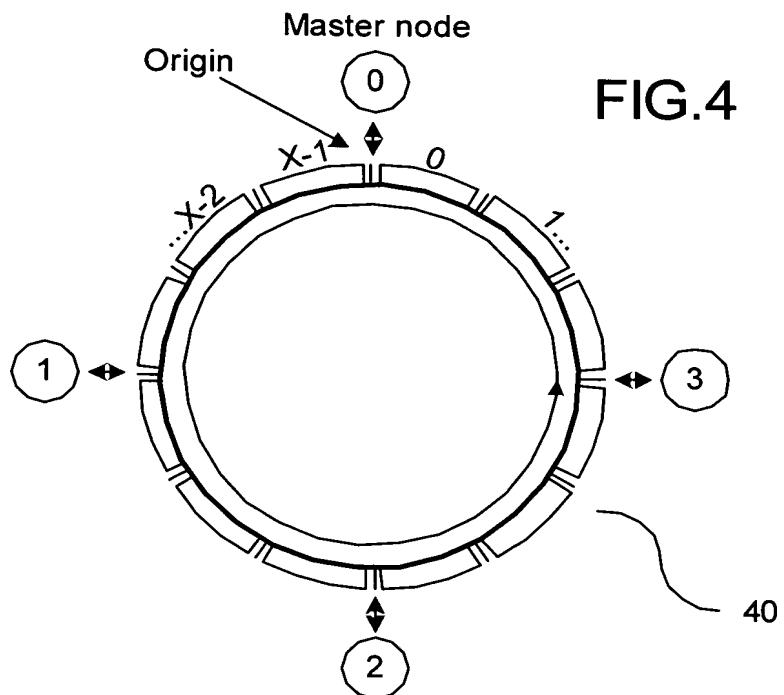
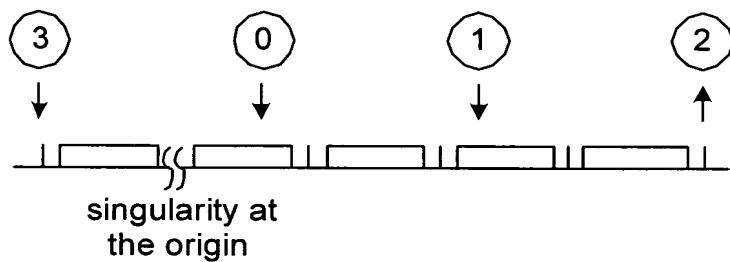
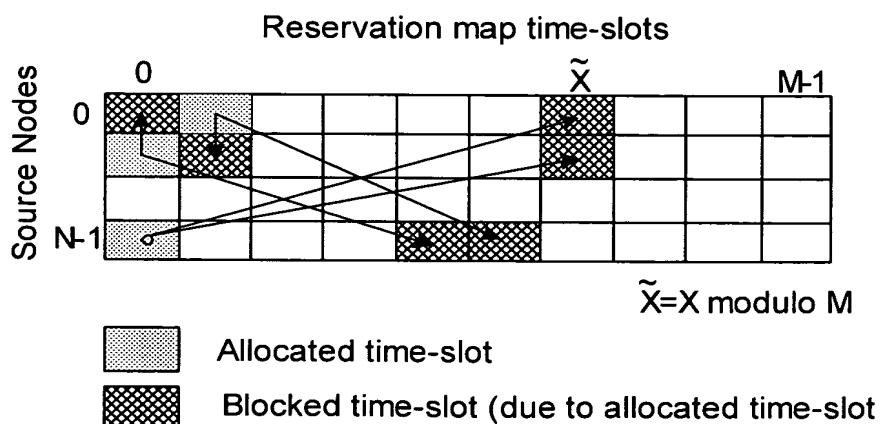
**FIG.5****FIG.6**

FIG.7

Destination Nodes		
Source Nodes	0	N-1
0	0	
1	0	
2		0
3		0

FIG.8

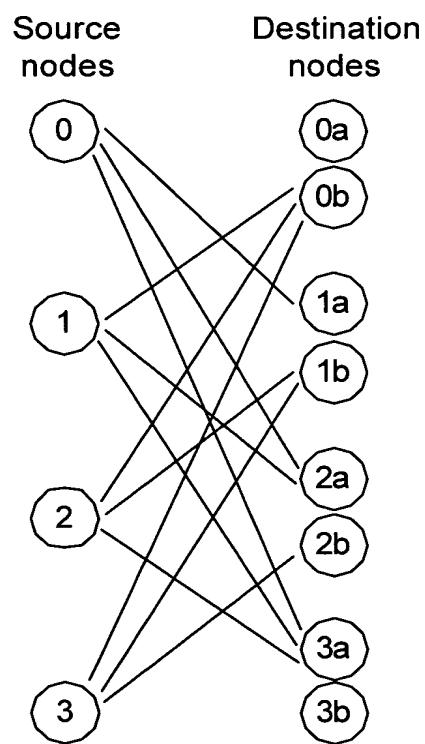


FIG.9

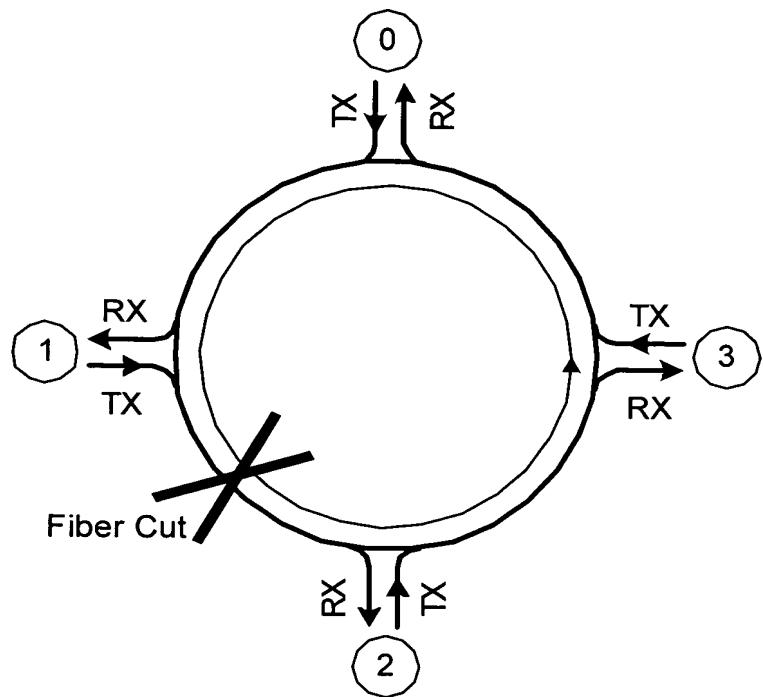


FIG.10

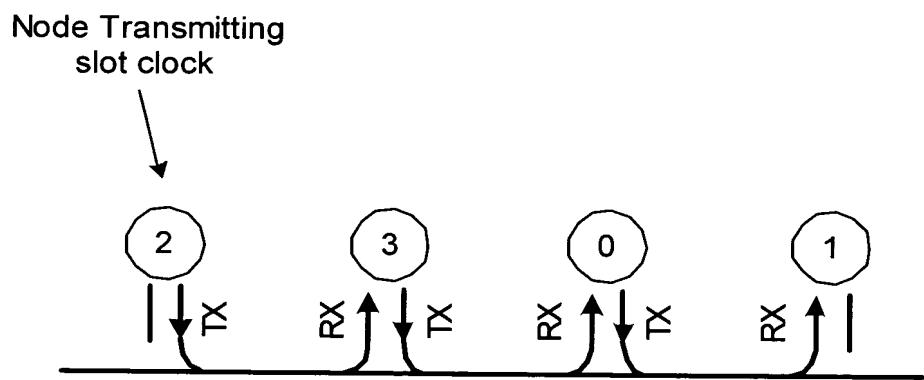


FIG.11

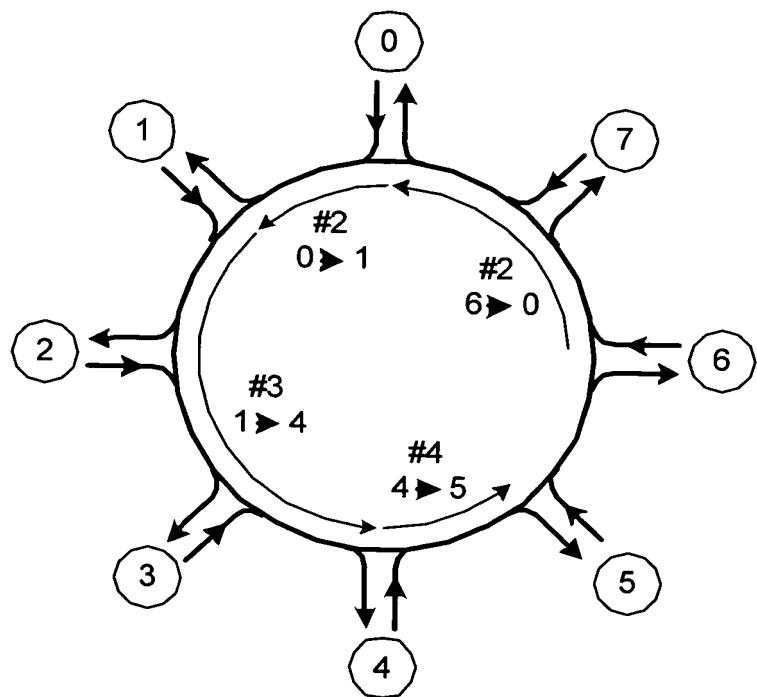


FIG.12

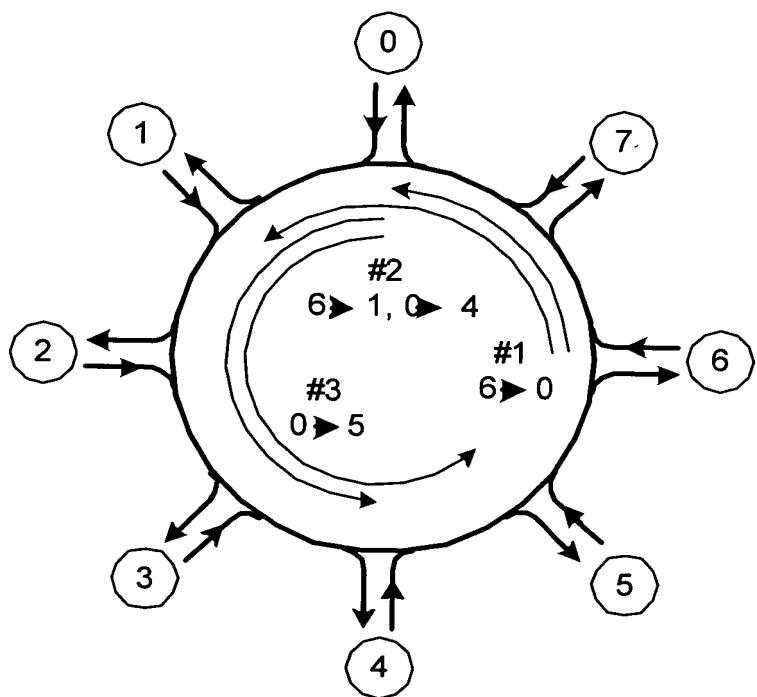


FIG.13

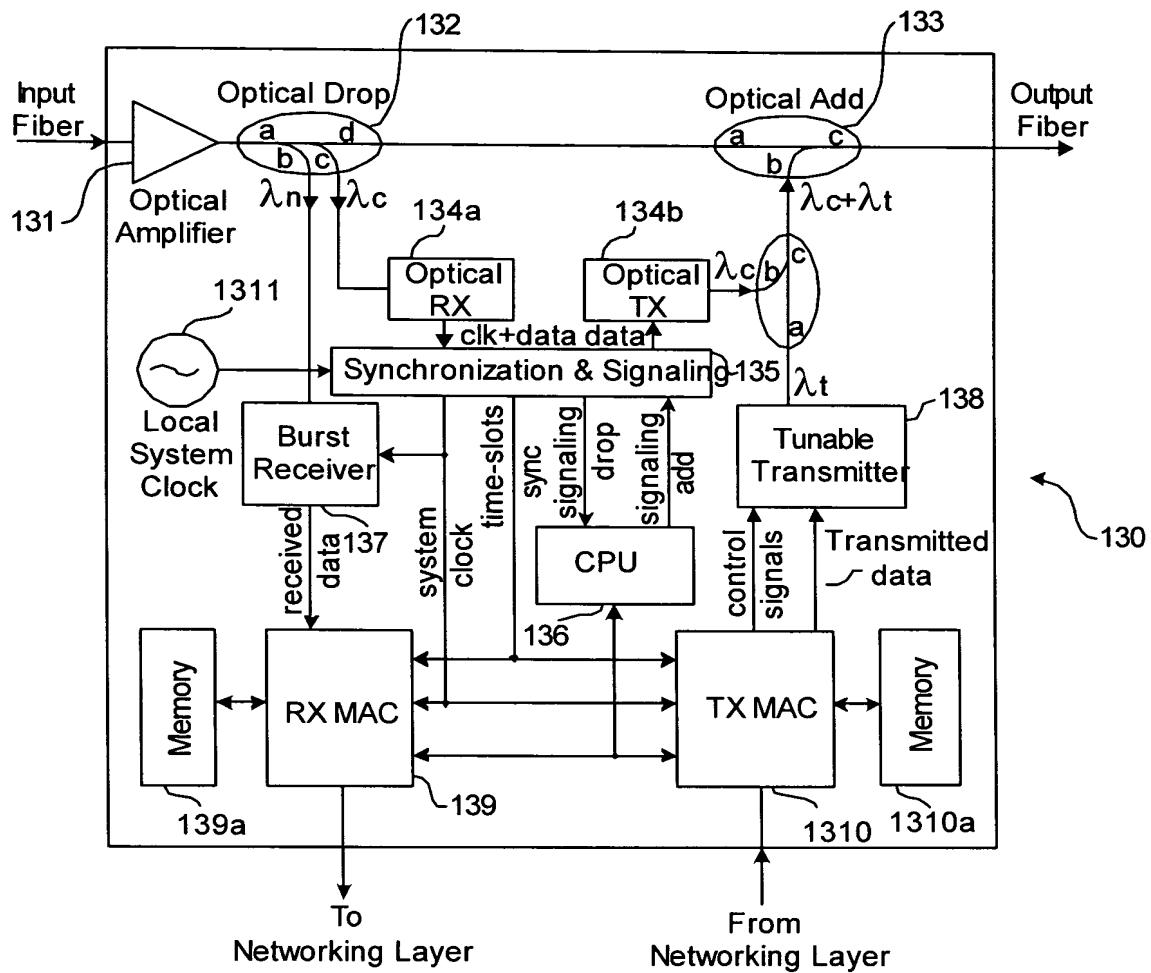


FIG. 14

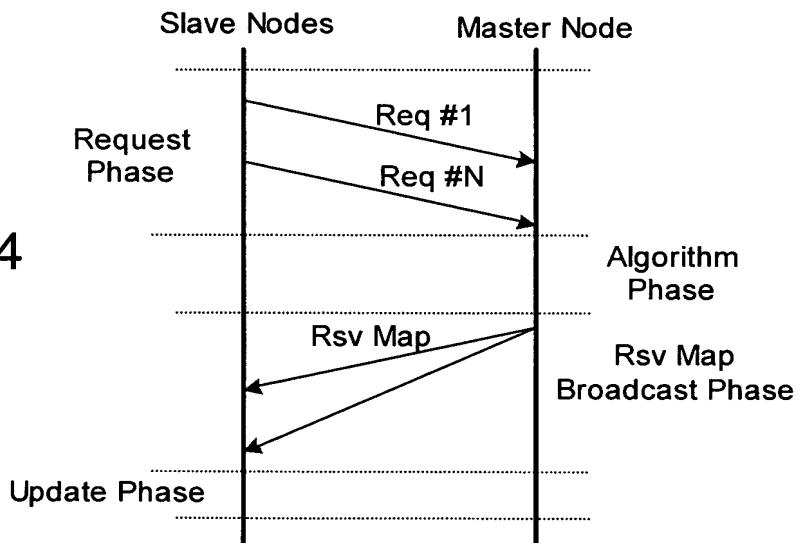


FIG.15

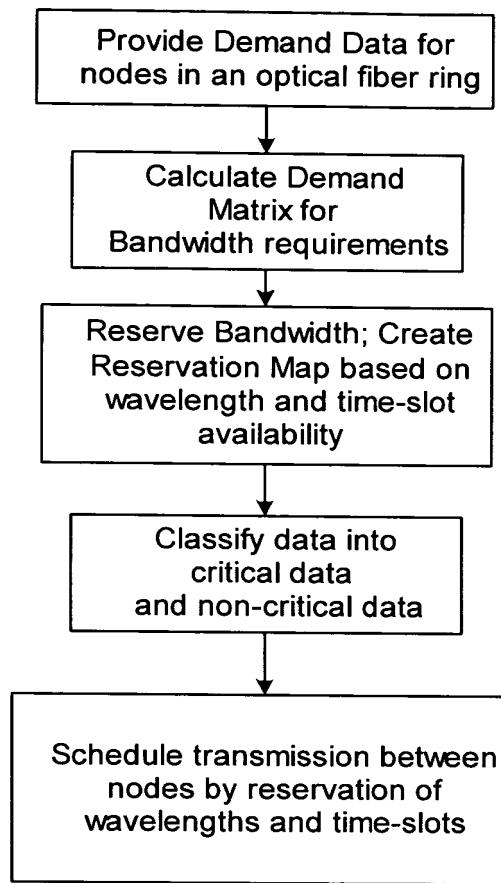
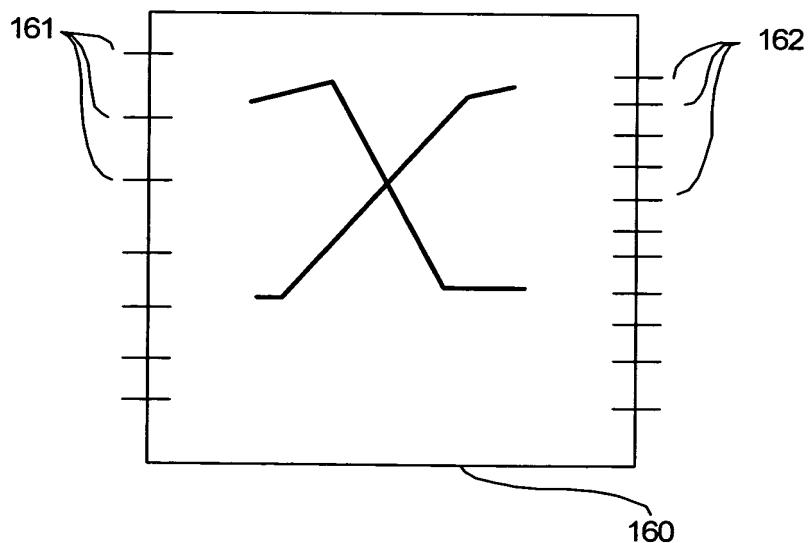


FIG.16



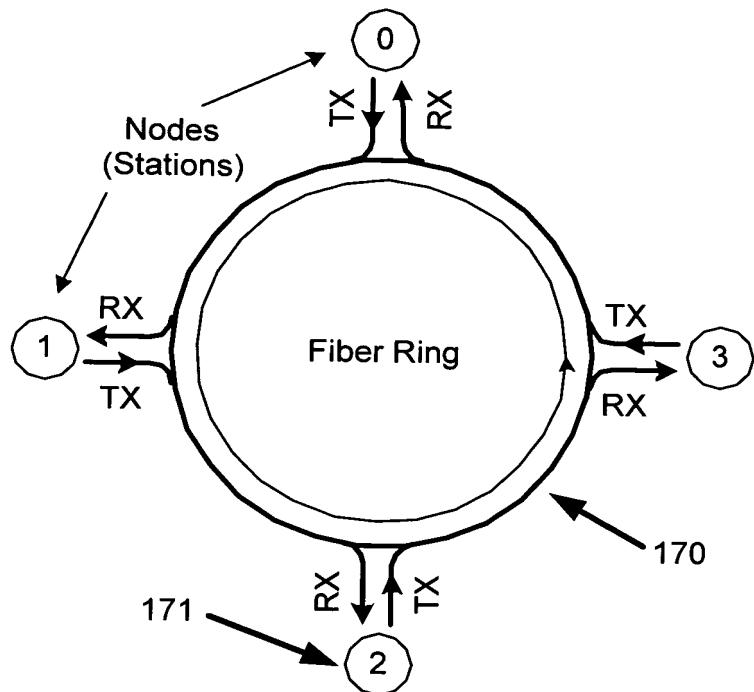


FIG.17A

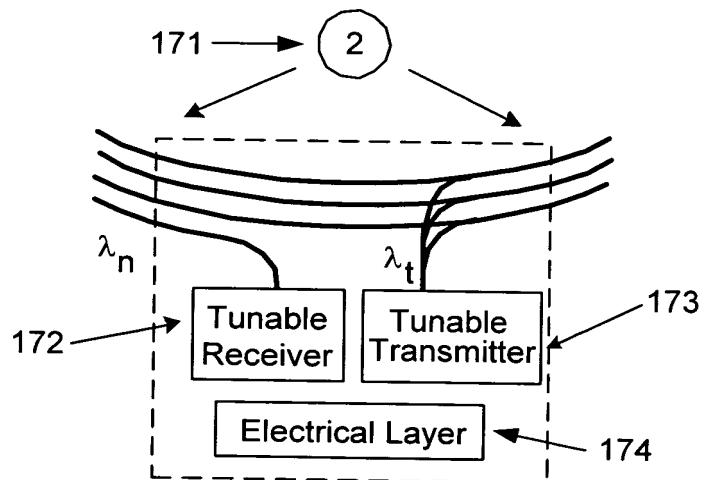


FIG.17B

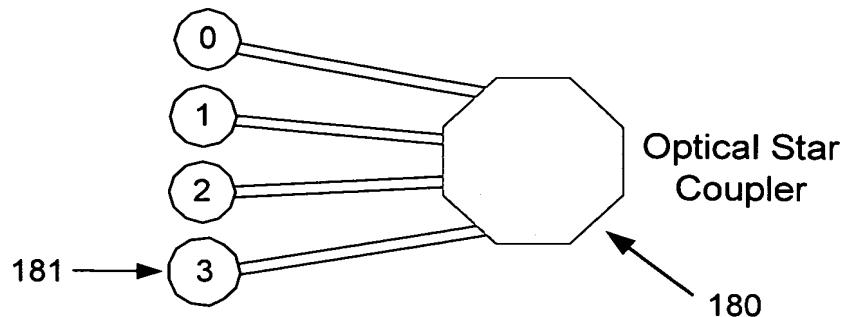


FIG. 18A

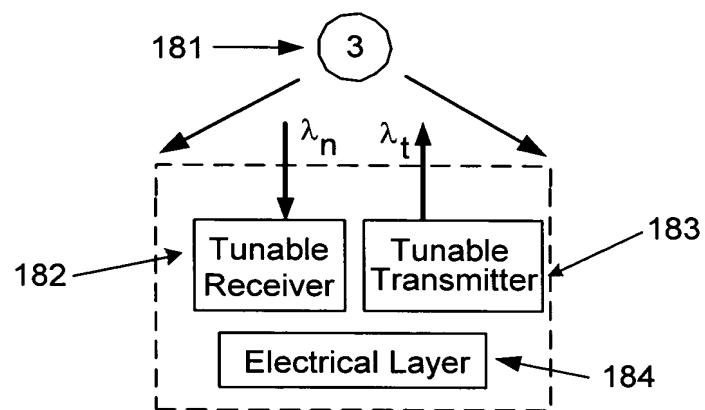


FIG. 18B

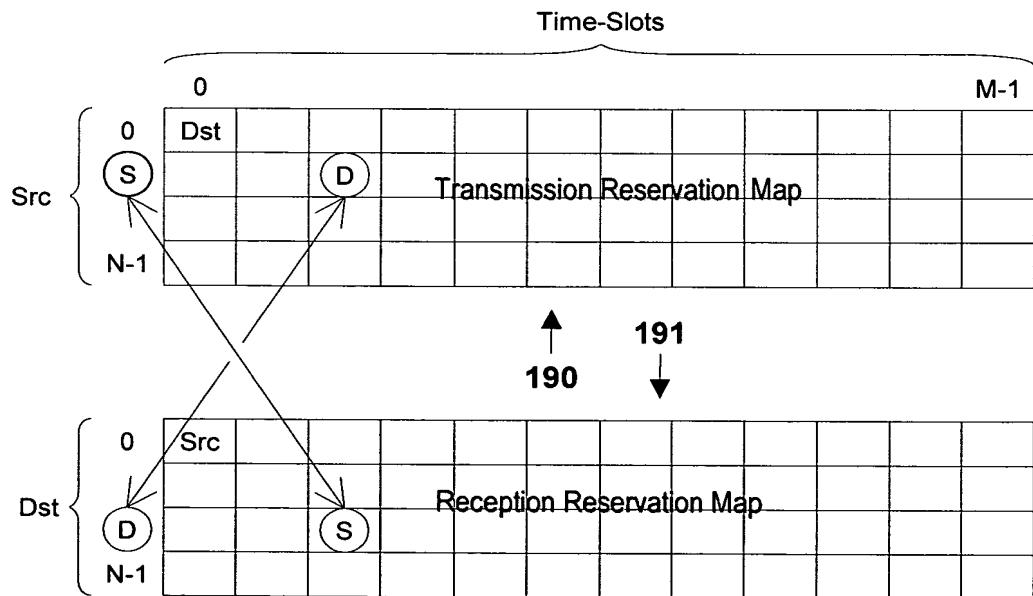


FIG. 19

